Please delete the present Abstract of the Disclosure.

Please add the following new Abstract of the Disclosure:

The correlation and demodulation circuit (6) in particular for a pseudo-random code radio-frequency signal receiver (1) includes a correlation stage (7) connected to ~~control means~~ a microprocessor (12) in particular for configuring ~~said~~ the correlation stage in normal operating mode or in test mode. In normal operation ~~said~~ the stage receives intermediate signals (IF) corresponding to the radio-frequency signals shaped in means (3) for receiving the modulated signals from the receiver. ~~Said~~ The intermediate signals are correlated in a correlator control loop (8) of said correlation stage (7) with a replica of the first code supplied by a code generator (25). The code generator (25) is adapted via ~~control means~~ a microprocessor (12) to generate a replica of a pseudo-random code of shorter repetition length than the pseudo-random code of the radio-frequency signals. Intermediate test signals (IF_{test}) with a reduced pseudo-random code are supplied to the correlation stage (7) so as to perform a test representative of the correlation stage in closed loop operation more quickly than with conventional intermediate signals.